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December 23, 1997

Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Attention: Certificate of Correction  
Publication Branch

Re: Certificate of Correction  
U.S. Patent No.: 5,679,977  
Our File: TESSERA 3.3-018  
**SEMICONDUCTOR CHIP ASSEMBLIES,  
METHODS OF MAKING SAME AND  
COMPONENTS FOR SAME**

Dear Sir:

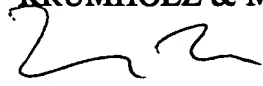
Enclosed herewith is an original and one copy of a Certificate of Correction with respect to the above-identified U.S. Patent.

The corrections indicated should be made as the application originally filed does not contain such errors. There is no requirement for payment of a fee since the errors were made by the Patent Office.

We look forward to early return of the copy of the Certificate of Correction duly certified.

Very truly yours,

**LERNER, DAVID, LITTENBERG,  
KRUMHOLZ & MENTLIK**

  
**MARCUS J. MILLET**  
Reg. No. 28,241

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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 5,679,977  
DATED : October 21, 1997  
INVENTOR(S) : Khandros et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Frontispiece, above [21] insert --PCT/US91/06920 September 24, 1991 and WO 92/05582, April 2, 1992--.

Column 35, line 29, change "8" to --7--.

Column 35, line 54, change "shootlike" to read --sheetlike--.

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FORM PTO 1050 (REV 3-82)

PATENT NO 5,679,977

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